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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: **Schulze et al.**)

MMB Docket No. **1890-0395**)

Examiner: **To be assigned**

Application No.: **10/587,413**)

Group Art Unit: **To be assigned**

Filed: **July 27, 2006**)

For: **Semiconductor Component**)
Comprising a Temporary Field)
Stopping Area, and Method for the)
Production Thereof)

I hereby certify that this correspondence is being deposited with
the United States Postal Service first class mail service under 37
CFR §1.10 on the date indicated below and is addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA
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July 31, 2007

(Date of Deposit)

David R. Moorman

Name of person mailing Document or Fee

David R. Moorman

Signature

July 31, 2007

Date of Signature

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, Applicant hereby discloses the following references,
copies of which are enclosed, regarding the above-identified patent application.

Foreign Application

Issue Date

Country

EP 0797 257 A2

24 September 1997

Europe

EP 1 014 453 A1

28 June 2000

Europe

DE 101 00 802 C1

22 August 2002

Germany

FORM PTO-1449

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MMB DOCKET NO.: 1890-0395

APPLICATION NO.: 10/587,413

APPLICANT(S): Schulze et al.

FILING DATE: July 27, 2006

GROUP ART UNIT: To be assigned



U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB-CLASS | FILING DATE |
|---------------------|----|--------------------|--------------|----------------|-------|-----------|-------------|
| | AA | US 4,752,818 | 21 June 1988 | Kushida et al. | | | |
| | AB | | | | | | |
| | AC | | | | | | |
| | AD | | | | | | |
| | AE | | | | | | |
| | AF | | | | | | |
| | AG | | | | | | |
| | AH | | | | | | |
| | AI | | | | | | |
| | AJ | | | | | | |
| | AK | | | | | | |

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |
|---------------------|----|---------------------|-------------------|---------|-------|-----------|-------------|
| | AL | EP 0797257 A2 | 24 September 1997 | Europe | | | Yes No |
| | AM | EP 1014453 A1 | 28 June 2000 | Europe | | | Yes No |
| | AN | DE 101 00 802 C1 | 22 August 2002 | Germany | | | Yes No |
| | AO | | | | | | Yes No |
| | AP | | | | | | Yes No |

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

| | | | |
|--|----|----------|---|
| | AQ | <u>1</u> | Niedernostheide, F.-J. et al. "13-kV Rectifiers: Studies on Diodes and Asymmetric Thyristors." <i>15th International Symposium on Power Semiconductor Devices & ICs</i> , Cambridge, April 14-17, 2003 (4 pages). |
| | AR | <u>1</u> | |
| | AS | <u>1</u> | |

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants.

U.S. Patent No.
US 4,752,818

Issue Date
21 June 1988

Inventor
Kushida et al.

Articles

- 1) Niedernostheide, F.-J. et al. "13-kV Rectifiers: Studies on Diodes and Asymmetric Thyristors." *15th International Symposium on Power Semiconductor Devices & ICs*, Cambridge, April 14-17, 2003. Pgs 122-125. (4 pages).


CONCISE EXPLANATION OF FOREIGN LANGUAGE REFERENCES

DE 101 00 802 C1 was cited in a corresponding case. It is believed that DE 101 00 802 C1 may be relevant to claim 47 (as filed in Preliminary Amendment of July 26, 2006). DE 101 00 802 C1 discloses a semiconductor component having embedded p-conductive compensation columns into a n conductive drift region 5, as shown in FIG. 6 of DE 101 00 802 C1.

It is believed that no fees are due for the consideration of this Information Disclosure Statement. However, the Commissioner is hereby authorized to charge any deficiency or to credit any overpayment to Deposit Account No. 13-0014, but not to include any payment of issue fees.

Respectfully Submitted,

July 31, 2007
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